CORRECTIONS TO SLIDE 14, CLASS 23

- **If the magnitude of V_{GG} (V_{GS}) increases, V_G becomes more negative and the G-S becomes more reverse biased. Hence, a smaller V_{DS} is required to achieve pinched-off and the I_D at saturation will be smaller than I_{DSS}.**
- **To operate a JFET as an amplifier, the JFET has to be biased in the saturation region. This means that $V_{DS} \geq V_{GS} + V_p$. The triode region is for $V_{DS} \leq V_{GS} + V_p$. At pinch-off, $V_{DS(sat)} = V_{GS} + V_p$. Hence, if V_{GS} is more negative, $V_{DS(sat)}$ becomes smaller. (John Seymour, "Electronic Devices and Components, Second Edition, 1988, Longman).22**
- **V_p and I_{DSS} are the JFET parameters and specified in the data sheet.**